**SRIRANGAM SAI TEJA**

**PROFILE**

A joyful, upcoming Electronics and Communication engineer, looking for an entry-level position in a dynamic organization with special interest in areas like Digital Electronics, VLSI, Control systems and related fields, resulting in a good quality contribution towards organizational growth along with personal and professional development.

**EDUCATION**

* **B.Tech Electronics and Communication Engineering**

**CGPA – 8.57 / 10 2019-2023**

Amrita Vishwa Vidyapeetham

* **Class 12** – 96.9% **2019**

Institution:

* **Class 10** – 98% **2017**

Institution:

**TECHNICAL INTERESTS**

Digital electronics, VLSI, Control systems.

**PROJECTS**

**Traffic light controller system using Verilog.**

Duration/Period: 15 days.

Objective: To design a traffic light controller for four-way intersection which has priority to emergency vehicles.

Tools or techniques used: ModelSim

**Temperature alert system.**

Duration/Period: 15 days.

Objective: To maintain safe range operation of temperature-controlled chamber.

Tools or techniques used: LT Spice

**Automated fan.**

Duration/Period: 1 Month

Objective: To design an automated fan which saves power.

Tools or techniques used: Arduino IDE.

**TECHNICAL SKILLS**

ModelSim, LT spice, HFSS, C, Arduino

**INTERNSHIP**

**BHEL Hyderabad.**

Duration/Period: From 20th June 2022 to 4th July 2022

Objective: To get familiarized with CNC and PLC.

Outcome: Studied about CNC and PLC and their usage in industries.

**CERTIFICATIONS**

**CLAD Certification**

Area / Topic / Details: LabVIEW proficiency Certification Course achieved 80%

When & Where: 1st May 2021 on online platform

**LANGUAGES**

English, Telugu, Hindi